

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Ji-Young KIM, et al.

Serial No.: 10/699,047

Examiner: Duong, Khanh B.

Filed: October 30, 2003

Art Unit: 2822

Confirmation No.: 1200

For: METHOD OF FABRICATING A MOSFET HAVING A RECESSED  
CHANNEL

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR  
ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance, the Examiner recited portions of the allowed independent claims and stated that none of the prior art fairly shows or suggests the recited portions. The applications note that it is a well-known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated some reason for allowance, there can be other reasons that claims are allowable.

**Customer No. 20575**

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.



Hosoon Lee  
Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C.  
210 SW Morrison Street, Suite 400  
Portland, OR 97204  
503-222-3613